	Application No.	Applicant(s)
Notice of Allowability	• •	
	10/787,223 Examiner	YAMAMOTO ET AL. Art Unit
	-	
	Kevin Quinto	2826
The MAILING DATE of this communication appears on the cover sheet with the correspondence address All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS. This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.		
1. ☐ This communication is responsive to 28 June 2005.		NAYHAN J. FLYNN
2. ☑ The allowed claim(s) is/are <u>1-9</u> .	5	SUPERVISORY PATENT EXAMINER
3. ☐ Acknowledgment is made of a claim for foreign priority un a) ☐ All b) ☐ Some* c) ☐ None of the:		TECHNOLOGY CENTER 2800
 Certified copies of the priority documents have been received. 		
2. ☑ Certified copies of the priority documents have been received in Application No. <u>10/657,106</u> .		
3. Copies of the certified copies of the priority documents have been received in this national stage application from the		
International Bureau (PCT Rule 17.2(a)).		
* Certified copies not received:		
Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application. THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.		
4. A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.		
5. CORRECTED DRAWINGS (as "replacement sheets") must be submitted.		
(a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached		
1) ☐ hereto or 2) ☐ to Paper No./Mail Date		
(b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date		
Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).		
6. DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.		
Attachment(s)		
1. Notice of References Cited (PTO-892)		Patent Application (PTO-152)
2. Notice of Draftperson's Patent Drawing Review (PTO-948)	6. ☐ Interview Summar Paper No./Mail D	
3. Information Disclosure Statements (PTO-1449 or PTO/SB/08 Paper No./Mail Date	Paper No./Mail D 8), 7. ⊠ Examiner's Amen	dment/Comment
Examiner's Comment Regarding Requirement for Deposit of Biological Material	8. Examiner's Staten	nent of Reasons for Allowance
o, biological iviatorial	9. Other	

EXAMINER'S AMENDMENT

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Tomoki Tanida on September 16, 2005.

The application has been amended as follows:

- a. In claim 6, change the phrase "its collector" to -- a collector --.
- b. In claim 6, change the phrase "its base" to a -- a base --.
- c. In claim 8, change the phrase "the emitter" to -- an emitter --.
- d. In claim 9, change the phrase "its collector" to -- a collector --.
- 2. Claims 1-9 are allowed.
- 3. The following is an examiner's statement of reasons for allowance: the examiner is unaware of any prior art which suggests a semiconductor device having a surge protection circuit with a Zener diode and a bipolar transistor such that the Zener diode has a cathode region connected to a terminal and an additional cathode region (while the cathode regions are not in contact with each other) in a semiconductor substrate such that the collector of the transistor includes an additional layer within the substrate and a buried layer within this additional layer.

4. Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Kevin Quinto whose telephone number is (571) 272-1920. The examiner can normally be reached on M-F 8AM-5PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Nathan Flynn can be reached on (571) 272-1915. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).